

Serial No. 09/414226  
Chang 7-20-10

IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

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JUN -7 2001

TC 2800 MAIL ROOM



Patent Application

Inventor(s): Chorng-Ping Chang  
Chien-Shing Pai  
Thi-Hong-Ha Vuong

Case: 7-20-10

Serial No.: 09/414,226

Group Art Unit: 2811

Filing Date: October 7, 1999

Examiner: Donghee Kang

Title: Semiconductor Device with Tapered Gate and Process For  
Fabricating the Device

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

**AMENDMENT AND RESPONSE TO RESTRICTION REQUIREMENT**

This reply is in response to the Election/Restriction mailed May 1, 2001.

**IN THE CLAIMS**

Please cancel claims 4-20, without prejudice, from the present application.

**REMARKS**

This reply is in response to the restriction requirement mailed May 1, 2001. The Examiner designated the invention recited in claims 1-3 (Group I) as drawn to a semiconductor device and the invention recited in claims 4-20 (Group II) as drawn to a process for making a semiconductor device. The applicants hereby elect to prosecute the Group I claims. Accordingly, the Group II claims are canceled, without prejudice, from the present application. The applicants make this election without traverse.

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PATENT APPLICATION

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Thi-Hong-Ha Vuong

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Title Semiconductor Device With Tapered Gate And Process For Fabricating The Device

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SIR:

Enclosed is an amendment in the above-identified application.

NO ADDITIONAL FEE REQUIRED

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 50-1735** as required to correct the error.

Respectfully,

*Richard J. Botos*  
Richard J. Botos, Attorney  
Reg. No. 32016  
908-582-3809.

Date: 5/31/01

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on May 31, 2001

*Patricia M. Holt*  
Date 5/31/01